



# MPR121 Capacitance Sensing Settings

## INTRODUCTION

Touch acquisition takes a few different parts of the system in order to detect touch. The first stage of this process is to capture the pad capacitance. Freescale's MPR121 utilizes the principle that a capacitor holds a fixed amount of charge at a specific electric potential. Both the implementation and the configuration will be described in this application note.

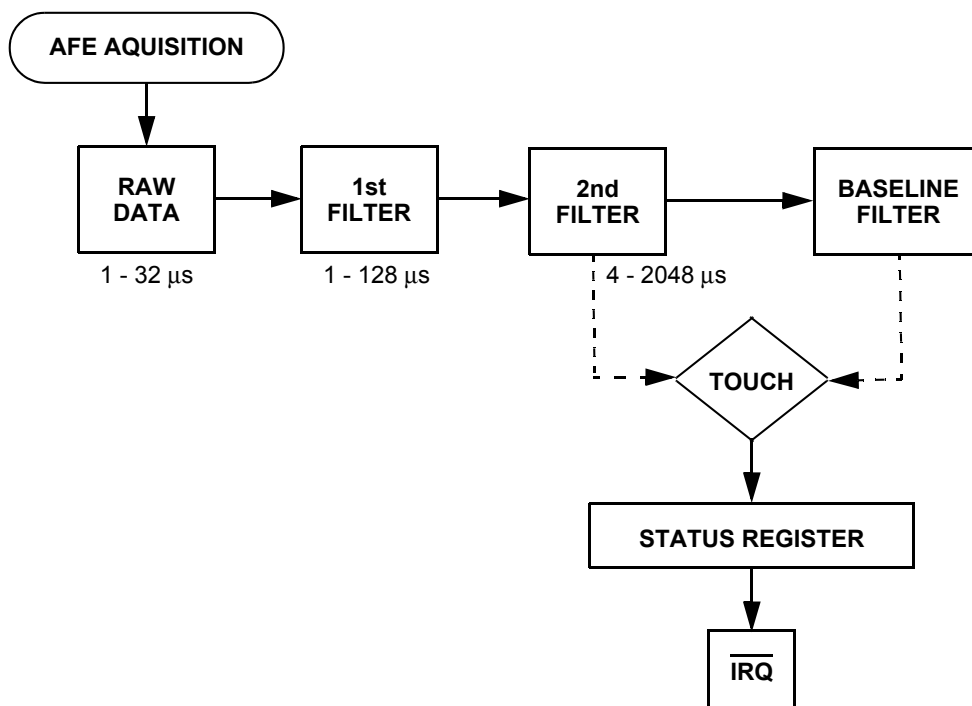


Figure 1. Data Flow in the MPR121

## CAPACITANCE MEASUREMENT

The basic measurement technique used by the MPR121 is to charge up the capacitor C on one electrode input with a DC current I for a time T (the charge time). Before measurement, the electrode input is grounded, so the electrode voltage starts from 0 V and charges up with a slope, Equation 1, where C is the pad capacitance on the electrode (Figure 2). All of the other electrodes are grounded during this measurement. At the end of time T, the electrode voltage is measured with a 10 bit ADC. The voltage is inversely proportional to capacitance according to Equation 2. The electrode is then discharged back to ground at the same rate it was charged.

$$\frac{dV}{dt} = \frac{I}{C} \quad \text{Equation 1}$$

$$V = \frac{I \times T}{C} \quad \text{Equation 2}$$

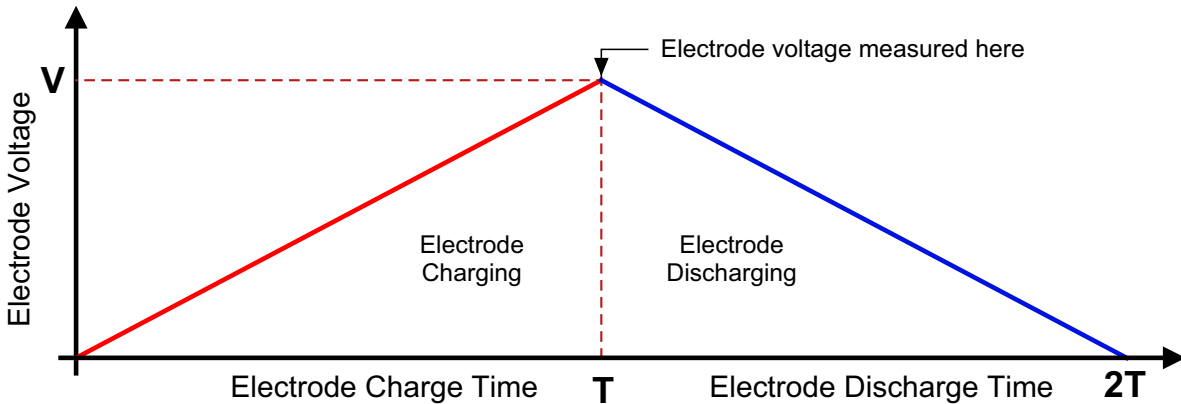


Figure 2. MPR121 Electrode Measurement Charging Pad Capacitance

When measuring capacitance there are some inherent restrictions due to the methodology used. On the MPR121 the voltage after charging must be in the range that is shown in Figure 2.

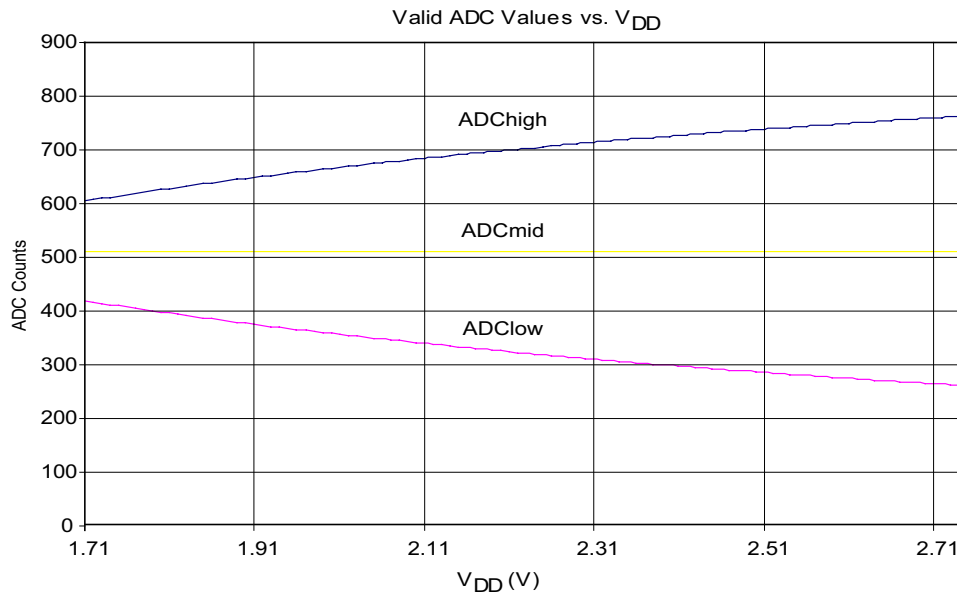


Figure 3.

The valid operating range of the electrode charging source is 0.7 V to ( $V_{DD} - 0.7$  V). This means that for a given  $V_{DD}$  the valid ADC (voltage visible to the digital interface) range is given by

$$ADC_{low} = \frac{0.7}{V_{DD}}(1024), \quad \text{Equation 3}$$

$$\text{and}$$

$$ADC_{high} = \frac{V_{DD} - 0.7}{V_{DD}}(1024). \quad \text{Equation 4}$$

These equations are represented in the graph. In the nominal case of  $V_{DD} = 1.8$  V the ADC range is shown below in [Table 1](#).

**Table 1.**

$V_{DD}$	$ADC_{high}$	$ADC_{low}$	$ADC_{mid}$
1.8	625.7778	398.2222	512

Any ADC counts outside of the range shown are invalid and settings must be adjusted to be within this range. If capacitance variation is of importance for an application after the current output, charge time and supply voltage are determined then the following equations can be used. The valid range for capacitance is calculated by using the minimum and maximum ADC values in the capacitance equation. Substituting the low and high ADC equations into the capacitance equation yields the equations for the minimum and maximum capacitance values which are

$$C_{low} = \frac{I \times T}{V_{DD} - 0.7} \quad \text{and} \quad C_{high} = \frac{I \times T}{0.7}. \quad \text{Equation 5}$$

## SENSITIVITY

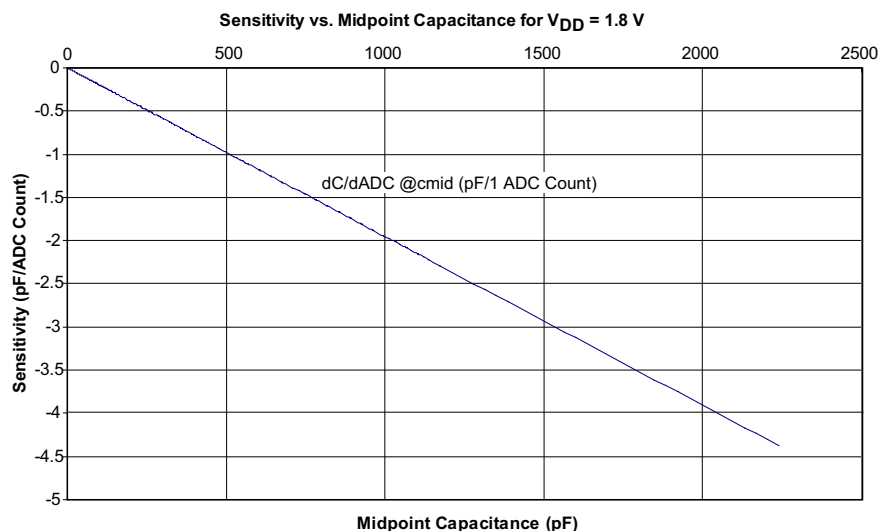
The sensitivity of the MPR121 is relative to the capacitance range being measured. Given the ADC value, current and time and settings capacitance can be calculated,

$$C = \frac{I \times T \times 1024}{V_{DD} \times ADC}. \quad \text{Equation 6}$$

For a given capacitance the sensitivity can be measured by taking the derivative of this equation. The result of this is the following equation, representing the change in capacitance per one ADC count, where the ADC in the equation represents the current value.

$$\frac{dC}{dADC} = \frac{I \times T \times 1024}{V_{DD} \times ADC^2} \quad \text{Equation 7}$$

This relationship is shown in the following graph by taking the midpoints off all possible ranges by varying the current and time settings. The midpoint is assumed to be 512 for ADC and the nominal supply voltage of 1.8 V is used.



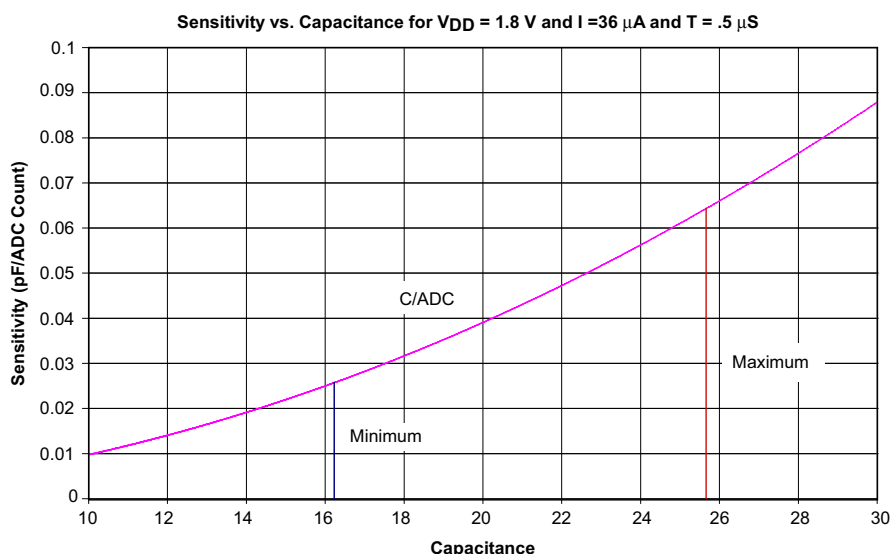
**Figure 4.**

Smaller amounts of change indicate increased sensitivity for the capacitance sensor. Some sample values are shown in Table 2.

**Table 2.**

pF	Sensitivity (pF/ADC count)
10	-0.01953
100	-0.19531

In the previous cases, the capacitance is assumed to be in the middle of the range for specific settings. Within the capacitance range the equation is nonlinear, thus the sensitivity is best with the lowest capacitance. This graph shows the sensitivity derivative reading across the valid range of capacitances for a set  $I$ ,  $T$ , and  $V_{DD}$ . For simple small electrodes (that are approximately 21 pF) and a nominal 1.8 V supply, Figure 5 is representative of this effect.



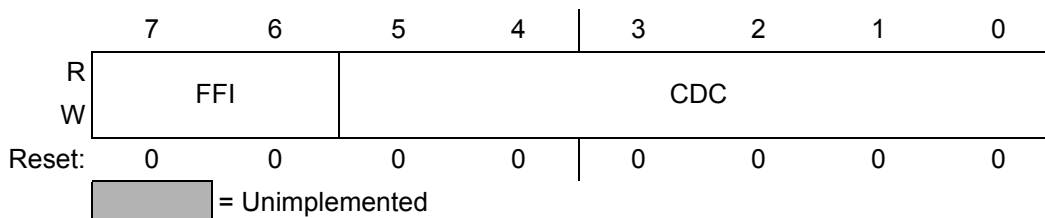
**Figure 5.**

## CONFIGURATION

From the implementation above, there are two elements that can be configured to yield a wide range of capacitance readings ranging from 0.455 pF to 2874.39 pF. The two configurable components are the electrode charge current and the electrode charge time. The electrode charge current can be configured to equal a range of values between 1  $\mu\text{A}$  and 63  $\mu\text{A}$ . This value is set in the Charge Discharge Current (CDC) in the Analog Front End AFE Configuration register. The electrode charge time can be configured to equal a range of values between 500 ns and 32  $\mu\text{S}$ . This value is set in the Charge Discharge Time (CDT) in the Filter Configuration Register.

### AFE CONFIGURATION REGISTER

The AFE Configuration Register is used to set both the CDC and the number of samples taken in the lowest level filter. The address of the AFE Configuration Register is 0x5C.

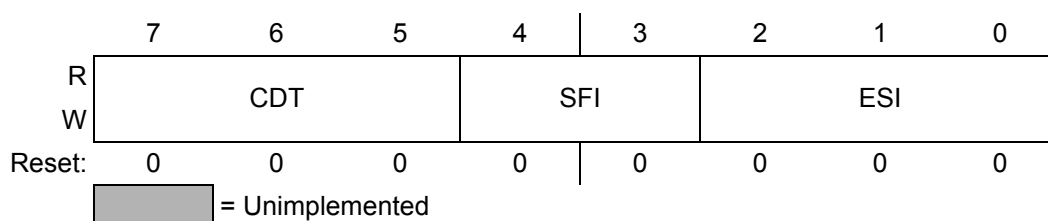


**Figure 6. AFE Configuration Register**

**Table 3. AFE Configuration Register Field Descriptions**

Field	Description
7:6 FFI	First Filter Iterations – The first filter iterations field selects the number of samples taken as input to the first level of filtering. 00 Encoding 0 – Sets samples taken to 6 01 Encoding 1 – Sets samples taken to 10 10 Encoding 2 – Sets samples taken to 18 11 Encoding 3 – Sets samples taken to 34
5:0 CDC	Charge Discharge Current – The Charge Discharge Current field selects the supply current to be used when charging and discharging an electrode. 000000 Encoding 0 – Disables Electrode Charging 000001 Encoding 1 – Sets the current to 1 $\mu$ A ~ 111111 Encoding 63 – Sets the current to 63 $\mu$ A

#### FILTER CONFIGURATION REGISTER



**Figure 7. Filter Configuration Register**

**Table 4. Filter Configuration Register Field Descriptions**

Field	Description
7:5 CDT	Charge Discharge Time – The Charge Discharge Time field selects the amount of time an electrode charges and discharges. 000 Encoding 0 – Invalid 001 Encoding 1 – Time is set to 0.5 $\mu$ s 010 Encoding 2 – Time is set to 1 $\mu$ s ~ 111 Encoding 7 – Time is set to 32 $\mu$ s.
4:3 SFI	Second Filter Iterations – The Second Filter Iterations field selects the number of samples taken for the second level filter. 00 Encoding 0 – Number of samples is set to 4 01 Encoding 1 – Number of samples is set to 6 10 Encoding 2 – Number of samples is set to 10 11 Encoding 3 – Number of samples is set to 18
2:0 ESI	Electrode Sample Interval – The Electrode Sample Interval field selects the period between samples used for the second level of filtering. 000 Encoding 0 – Period set to 1 ms 001 Encoding 1 – Period set to 2 ms ~ 111 Encoding 7 – Period set to 128 ms

The SFI, ESI and FFI are described in AN3890. In addition to these global (same for all electrodes) settings, the MPR121 electrodes can also be independently configured.

## ELECTRODE CHARGE CURRENT REGISTER

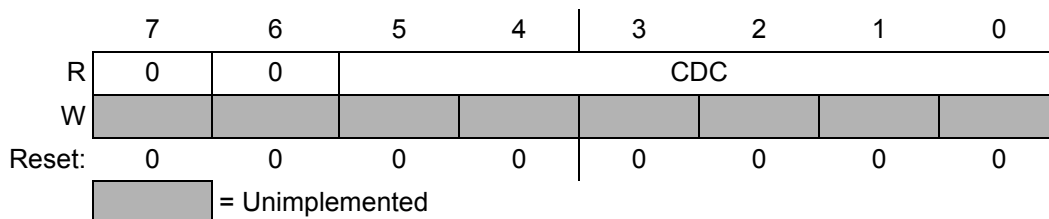


Figure 8. Electrode Charge Current Register

Table 5. Electrode Charge Current Register Field Descriptions

Field	Description
5:0 CDC	Electrode # Charge Discharge Current – The Charge Discharge Current field selects the supply current to be used when charging and discharging an electrode. 000000 Encoding 0 – Disables Electrode Charging 000001 Encoding 1 – Sets the current to 1µA ~ 111111 Encoding 63 – Sets the current to 63 µA

## ELECTRODE CHARGE TIME

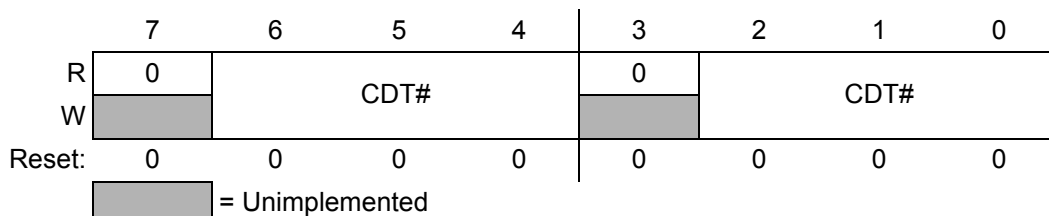


Figure 9. Electric Charge Time Register

Table 6. Electrode Charge Time Register Field Descriptions

Field	Description
6:4 CDT#	Electrode # Charge Discharge Time – The Charge Discharge Time field selects the amount of time an electrode charges and discharges. 000 Encoding 0 – Global value is used. 001 Encoding 1 – Time is set to 0.5 µs 010 Encoding 2 – Time is set to 1 µs ~ 11 Encoding 7 – Time is set to 32 µs.
2:0 CDT#	Electrode # Charge Discharge Time – The Charge Discharge Time field selects the amount of time an electrode charges and discharges. 000 Encoding 0 – Global value is used. 001 Encoding 1 – Time is set to 0.5 µs 010 Encoding 2 – Time is set to 1 µs ~ 11 Encoding 7 – Time is set to 32 µs.

## AUTO-CONFIGURATION

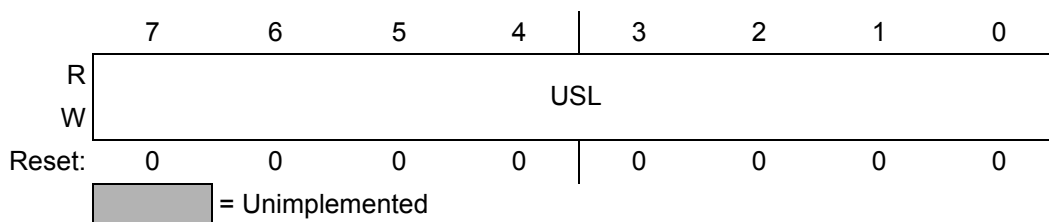
One of the new features added in the MPR121 that was not included in the MPR03X is the ability to automatically configure the Charge Current the Charge Time. This eliminates much of the guess involved with touch sensors and allows the same settings to properly configure the device for a wide range of application and electrodes. As show earlier in this document, the sensitivity of the sensor is maximized by having the baseline be as high as possible for a specific baseline capacitance. The restriction on the high side is that a system should not charge above  $V_{DD} - 0.7$  V due to this being a non-linear region. Thus the target voltage used is approximately  $V_{DD} - 0.7$  V.

**Table 7.**

Voltage ( $V_{DD}$ )	$V_{DD} - 0.7$ V	ADC	Baseline
1.8 V	1.1 V	625	156
$V_{DD}$	2.3 V	785	196

This implies that the automatic configuration system should target approximately 156 when  $V_{DD}$  is 1.8 V and 196 when  $V_{DD}$  is 3.0 V. The following three registers should be set based on the  $V_{DD}$  in the system. If the voltage is unregulated, set the values assuming the lowest voltage necessary for the battery. If the final voltage supply in the system is not known, just use the 1.8 V values as they represent the worst case. This lower setting will not dramatically affect the performance, thus the 1.8 V could be considered default and be used in all cases where fine tuning is not required.

## AUTO-CONFIG USL REGISTER



**Figure 10. AUTO-CONFIG USL Register**

**Table 8. AUTO-CONFIG USL Register Field Descriptions**

Field	Description
7:0 USL	Upper Limit – The Upper Limit for the auto-configuration baseline search is set to this value. 00000000 – Upper Limit set to 0 00000001 – Upper Limit set to 1 ~ 11111111 – Upper Limit set to 255

As this register represents the upper limit for the auto-configuration the value can be calculated by:

$$VSL = \frac{V_{DD} - 0.7}{V_{DD}} \cdot 256 \quad \text{Equation 8}$$

For the 1.8 V system, this value is 156 or 0x9C.

## AUTO-CONFIG TARGET LEVEL REGISTER

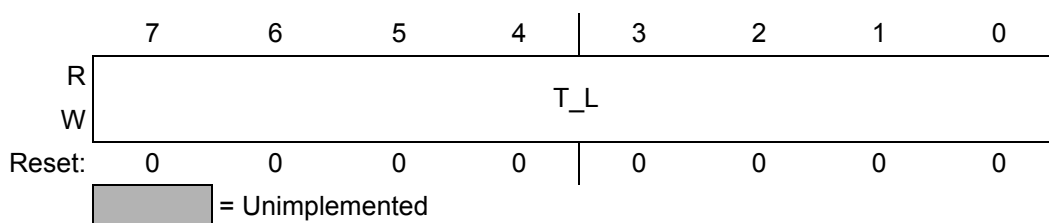


Figure 11. AUTO-CONFIG Target Level Register

Table 9. AUTO-CONFIG Target Level Register Field Descriptions

Field	Description
7:0 TL	Target Level – The Target Level for the auto-configuration baseline search is set to this value. 00000000 – Target Level set to 0 00000001 – Target Level set to 1 ~ 11111111 – Target Level set to 255

This register represents the target level for the auto-configuration. The value can be calculated by:

$$Target = \frac{V_{DD} - 0.7}{V_{DD}} \cdot 256 \cdot 0.9 \quad \leftarrow 90\% \text{ of USL}$$

For a 1.8 V system, this value is 140 or 0x8C.

## AUTO-CONFIG LSL REGISTER

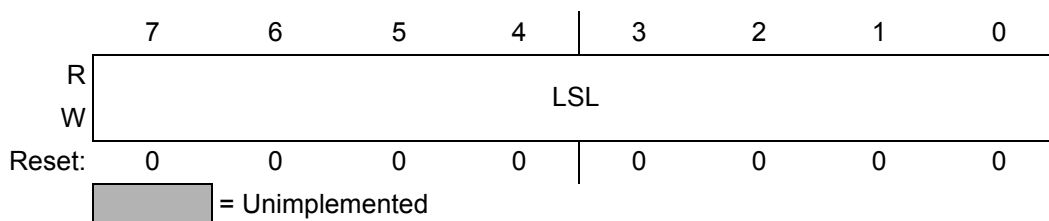


Figure 12. AUTO-CONFIG LSL Register

Table 10. AUTO-CONFIG LSL Register Field Descriptions

Field	Description
7:0 LSL	Lower Limit – The Lower Limit for the auto-configuration baseline search is set to this value. 00000000 – Lower Limit set to 0 00000001 – Lower Limit set to 1 ~ 11111111 – Lower Limit set to 255

This register represents the lower limit for the auto-configuration. The value can be calculated by:

$$Target = \frac{V_{DD} - 0.7}{V_{DD}} \cdot 256 \cdot 0.65 \quad \leftarrow 65\% \text{ of USL}$$

For a 1.8 V system, this value is 101 or 0x65.

The last setting required to set up the auto-configuration system is the AUTO e Register.



## AUTO-CONFIG CONTROL REGISTER

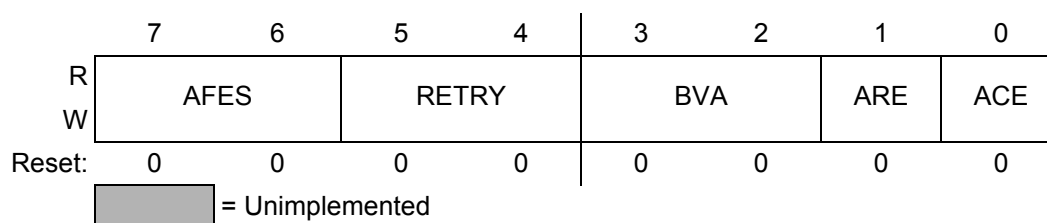


Figure 13. AUTO-CONFIG Control Register

Table 11. AUTO-CONFIG Control Register Field Descriptions

Field	Description
7:6 AFES	First Filter Iterations – The first filter iterations field selects the number of samples taken as input to the first level of filtering. This value must match the FFI set in the AFE Configuration register for proper AUTO-CONFIG functionality. 00 Encoding 0 – Sets samples taken to 6 01 Encoding 1 – Sets samples taken to 10 10 Encoding 2 – Sets samples taken to 18 11 Encoding 3 – Sets samples taken to 34
5:4 RETRY	Retry – The Retry value determines under what circumstances the auto-configuration system will retry. 00 – Retry disabled 01 – Retry enabled 10 – Retry enabled 11 – Retry enabled
3:2 BVA	Baseline Value Adjust – The baseline value adjust determines the initial value of the baseline registers after auto-configuration completes. 00 – Baseline is not changed 01 – Baseline is cleared 10 – Baseline is set to the AUTO-CONFIG baseline with the lower 3 bits cleared 11 – Baseline is set to the AUTO-CONFIG baseline
1 ARE	Automatic Reconfiguration Enable – The automatic reconfiguration enable, enables or disables automatic reconfiguration. 0 – ARE is disabled 1 – ARE is enabled
0 ACE	Automatic Configuration Enable – The automatic configuration enable, enables or disables automatic configuration. 0 – ACE is disabled 1 – ACE is enabled

The normal setup of the system is to set this register to 0x0B or 0b00001011. This means that the FFI is 00, but if the FFI in the AFE Configuration Register is different, it must be changed to match. For a description of this register, please refer to AN3890. The RETRY is disabled because in production systems, this will not be required. The BVA is set to 10 which allows the baseline to be updated. 10 is used instead of 11 because this guarantees that the baseline will be lower than the data. This is preferable as it protects against false touches. If somehow the baseline started higher than the data, a touch would be triggered and the detection system would have to be reset to work correctly. Last, both the automatic configuration and automatic reconfiguration are enable. Reconfiguration will trigger any time the baseline drifts outside the range set by the USL and the LSL.

There is also a set of flags which show when the automatic configuration has failed. For normal sized touch electrodes, this cannot occur without the USL, LSL and TSL being incorrectly set. The most likely configuration error is to set the USL (upper limit) at a lower value than the LSL (lower limit). Thus, as the algorithm searches for settings that work, it would always result in a fail throwing the OOR (Out Of Range) status flag.

The ARFF and ACFF also tell the user which type of configuration cycle caused the error. If it was triggered during an initial calibration, the ACFF will trigger. If the fail occurs during a reconfiguration, the ARFF will trigger.

#### ELE0-7 OUT OF RANGE STATUS REGISTER

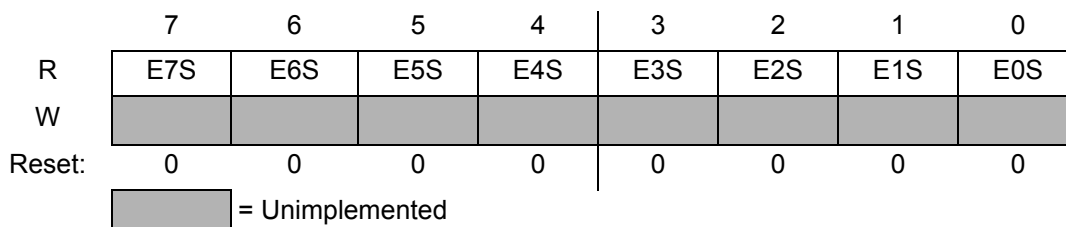


Figure 14. ELE0-7 Out Of Range Status Register

Table 12. ELE0-7 Out Of Range Status Register Field Descriptions

Field	Description
7 E7S	Electrode 7 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
6 E6S	Electrode 6 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
5 E5S	Electrode 5 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
4 E4S	Electrode 4 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
3 E3S	Electrode 3 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
2 E2S	Electrode 2 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
1 E1S	Electrode 1 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
0 E0S	Electrode 0 OOR Status – The Electrode 7 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed

## ELE8-11, ELEPROX OUT OF RANGE STATUS REGISTER

	7	6	5	4	3	2	1	0
R	ARFF	ACFF	0	ELEPROXS	E11S	E10S	E9S	E8S
W								
Reset:	0	0	0	0	0	0	0	0

= Unimplemented

Figure 15. ELE8-11, ELEPROX Out Of Range Status Register

Table 13. ELE8-11, ELEPROX Out Of Range Status Register Field Descriptions

Field	Description
7 ARFF	Automatic Reconfiguration Fail Flag – The Automatic Reconfiguration Fail Flag shows is the OOR was triggered during a reconfiguration cycle. 0 – Auto-reconfiguration did not cause the OOR flag 1 – Auto-reconfiguration did cause the OOR flag
6 ACFF	Automatic Configuration Fail Flag – The Automatic Configuration Fail Flag shows is the OOR was triggered during an initial configuration cycle. 0 – Auto-configuration did not cause the OOR flag 1 – Auto-configuration did cause the OOR flag
4 ELEPROXS	Electrode PROX OOR Status – The Electrode PROX OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
3 E11S	Electrode 11 OOR Status – The Electrode 11 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
2 E10S	Electrode 10 OOR Status – The Electrode 10 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
1 E9S	Electrode 9 OOR Status – The Electrode 9 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed
0 E8S	Electrode 8 OOR Status – The Electrode 8 OOR Status shows if the AUTO-CONFIG has failed. 0 – Auto-configuration Successful 1 – Auto-configuration Failed

## ***How to Reach Us:***

### **Home Page:**

[www.freescale.com](http://www.freescale.com)

### **Web Support:**

<http://www.freescale.com/support>

### **USA/Europe or Locations Not Listed:**

Freescale Semiconductor, Inc.  
Technical Information Center, EL516  
2100 East Elliot Road  
Tempe, Arizona 85284  
1-800-521-6274 or +1-480-768-2130  
[www.freescale.com/support](http://www.freescale.com/support)

### **Europe, Middle East, and Africa:**

Freescale Halbleiter Deutschland GmbH  
Technical Information Center  
Schatzbogen 7  
81829 Muenchen, Germany  
+44 1296 380 456 (English)  
+46 8 52200080 (English)  
+49 89 92103 559 (German)  
+33 1 69 35 48 48 (French)  
[www.freescale.com/support](http://www.freescale.com/support)

### **Japan:**

Freescale Semiconductor Japan Ltd.  
Headquarters  
ARCO Tower 15F  
1-8-1, Shimo-Meguro, Meguro-ku,  
Tokyo 153-0064  
Japan  
0120 191014 or +81 3 5437 9125  
[support.japan@freescale.com](mailto:support.japan@freescale.com)

### **Asia/Pacific:**

Freescale Semiconductor China Ltd.  
Exchange Building 23F  
No. 118 Jianguo Road  
Chaoyang District  
Beijing 100022  
China  
+86 010 5879 8000  
[support.asia@freescale.com](mailto:support.asia@freescale.com)

### **For Literature Requests Only:**

Freescale Semiconductor Literature Distribution Center  
1-800-441-2447 or +1-303-675-2140  
Fax: +1-303-675-2150  
[LDCForFreescaleSemiconductor@hibbertgroup.com](mailto:LDCForFreescaleSemiconductor@hibbertgroup.com)

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale and the Freescale logo are trademarks of Freescale Semiconductor, Inc., Reg. U.S. Pat. & Tm. Off.

All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc. 2010. All rights reserved.

