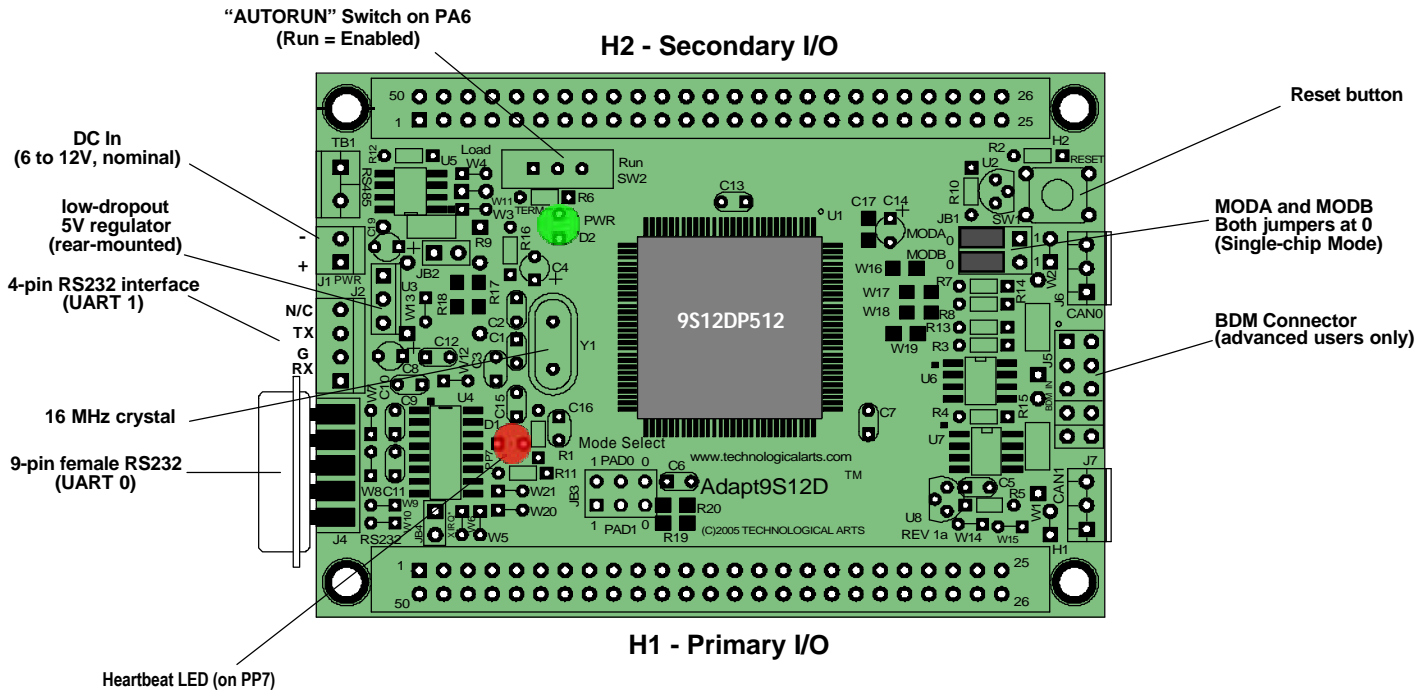


Adapt9S12DP512 Module with on-chip BASIC



CONNECTOR PIN ASSIGNMENTS

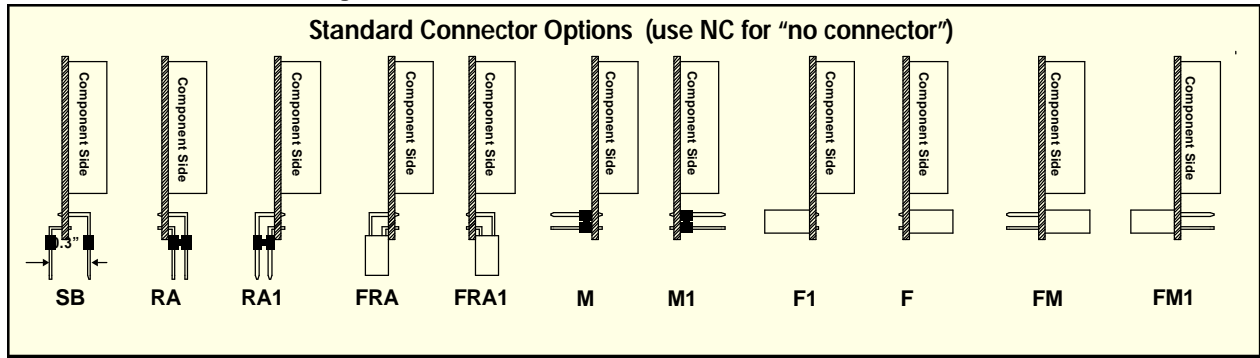
H1			H2				
PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME
1	PS4	50	GROUND	1	PA7	50	VCC (+5VDC)
2	PS5	49	GROUND	2	PA6 (Autorun Switch)	49	GROUND
3	PS6	48	PS0 (UART0 RXD)	3	PA5 - LCD-D7	48	PE7
4	PS7	47	+5VDC	4	PA4 - LCD-D6	47	PK7
5	PS1 (UART0 TXD)	46	PE1 (ZigFlea irq*)	5	PA3 - LCD-D5	46	PK5
6	PT7	45	PE0/XIRQ*	6	PA2 - LCD-D4	45	PK4
7	PT6	44	RESET*	7	PA1 - LCD-EN	44	PK3
8	PT5	43	PE7	8	PA0 - LCD-RS	43	PK2
9	PT4	42	PH0	9	PB7	42	PK1
10	PT3	41	PH1	10	PB6	41	PK0
11	PT2	40	PH2	11	PB5	40	PJ0
12	PT1	39	PH3	12	PB4	39	PJ7 (SCI)
13	PT0 - ToasterFan	38	PH4	13	PB3	38	PJ6 (SDA)
14	PP7 (Heartbeat LED)	37	PH5	14	PB2	37	PM7
15	PP6	36	PH6	15	PB1	36	PM6
16	PP5 - ToasterHeater	35	PH7	16	PB0	35	PM5/SCK
17	PP4 - Select	34	PS2/RXD1	17	PE2	34	PM4/MOSI
18	PP3 - Up	33	PE4 (ZigFlea attn*)	18	PE4 (ZigFlea attn*)	33	PM3/SS*
19	PP2 - Down	32	PS3/TXD1	19	PE3 (ZigFlea rxten)	32	PM2/MISO
20	PP1 - Right	31	VRL	20	PE1 (ZigFlea irq*)	31	PM1
21	PP0 - Left	30	VRH	21	PJ1 (ZigFlea rst*)	30	PM0
22	PAD00	29	PAD04	22	PAD08	29	PAD12
23	PAD01	28	PAD05	23	PAD09	28	PAD13
24	PAD02	27	PAD06	24	PAD10	27	PAD14
25	PAD03	26	PAD07	25	PAD11	26	PAD15

I2C pins

QSPI pins
(connect to
MAX6675)

ZigFlea pins¹

NOTES: * indicates active low signal



¹ - Recommended pins for implementing ZigFlea feature. Requires addition of external hardware.

Order Code: Module with StickOS BASIC in Flash: AD9S12DP512BM0-□-□

(NOTE: When ordering modules, fill in -□-□ with desired connector option codes for H1 and H2, as shown above)

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AD9S12DP512BMDAT1c